Certificate Course on Programming Logic and Design

Babu Jagjivan Ram Government Degree College
Narayanaguda, Hyderabad. (T.S.) 500029

REPORT ON CERTIFICATE COURSE (2017-2018)

1. **Name of the Course:** Certificate Course on Programming Logic and Design.

2. **Department:** Computer Science

3. **Duration:** 01.08.2017 to 22.08.2017   No. of hours – 30 Hours, No. of days - 15 Days

4. **Whether brochure containing details of curriculum, assessment, procedures, etc., is enclosed?** Yes

5. **Number of students enrolled:** 30 No.s

6. **Whether list of students is enclosed? (Yes/No):** Yes

7. **Brief Report of the Certificate Course along with the outcome:**

Objectives:

The certificate course is aimed to impart programming logic and design skills to the UG students especially aimed for the entry level UG Students.

Report:
A total of 30 Students have joined the certificate course. These students are quite punctual and found enthusiastic while learning the course.

Outcome: The course have shown profound improvement in programming logic and design skills in UG entry level students. The course also motivated these students to pursue next level advanced programming languages in forthcoming academic year.

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NOTICE

29.07.2017

This is to inform all the students of B.A, B.Com and B.Sc that the department of computer science is organizing a certificate course titled "Programming Logic and Design" to be scheduled from 01.08.2017 through 22.08.2017 for a period of 30 Hours. Interested students are requested to contact the in-charge, department of computer science and register for the course on or before 30.07.2017.
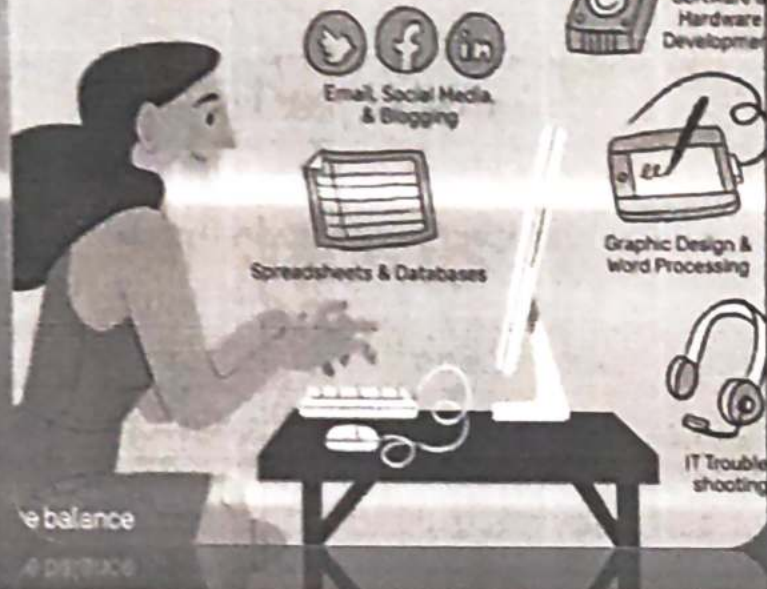
Principal
PRINCIPAL
B.J.R. Govt. Degree College
Vittal Wadi, Narayanguda,
Hyderabad. T.S.

BJR Government Degree College, Hyderabad
Department of Computer Science

Certificate Course on
Programming Logic and Design

About the Course: The certificate course is aimed to impart programming logic and design skills to the UG students especially aimed for the entry level UG Students

Duration: 01.08.2017 – 22.08.2017 (30 Hours)

Registration: Please register on or before 31.07.2017

Contact: Incharge, Department of Computer Science

Course Brochure

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Hyderabad. T.S.

Programming Logic and Design

Duration: 30

Syllabus

Hours	Topic
1-2.	Course Orientation, Using **Canvas**, Downloading resources
3-4	Computer Programming Introduction, Binary Numbering System
5-6	Input, Processing, Output; Variables, Data Types, Pseudo code
7-8	Flowcharting, Using Raptor© to flowchart
9-10	Modular Programming
11-12	Modular Programming
13-14	Decision Structures: Boolean Logic
15-16	Decision Structures: Case form
17-18	Looping Structures: While Do, Do Until
19-20	Looping Structures: For loop
21-22	Data Validation
23-24	Arrays, Processing Arrays with Loops
24-26	Object Oriented Programming Introduction
27-28	Class ,Object, Polymorphism
29-30	Inheritance, Dynamic Binding

List of Students - programming Logic & Design (2017-2018)

17011070111001	AMGOTHU REVANTH
17011070111002	A NITHIN KUMAR
17011070111003	ANUGULA MADHURI
17011070111004	ARUGONDA SAI GANESH
17011070111005	BANTU PRADEEP KUMAR
17011070111006	B GANESH
17011070111007	BIJIGIRI ARUN KUMAR
17011070111008	CHAVAN DHYANESHWAR
17011070111009	CHOWHAN PRAKASH
17011070111010	DUDDU MAHESHBABU
17011070129001	BANAVATH SRIRAM
17011070129002	BANOTH NAVEEN KUMAR
17011070129003	BANOTH PRAKASH
17011070129004	B HARISH
17011070129005	BHOOTHKURI VAMSHI
17011070129006	BOYA RAMESH
17011070129007	CHAKALI PAVAN KUMAR
17011070129008	C MAHESH
17011070129009	DANDUGULA NAVEEN
17011070129010	DAPPU GOPI
17011070156504	B RAJU
17011070156505	CH DURGAIAH
17011070156506	DANDEM PRASHANTH
17011070156507	GADDA RAJU
17011070156508	JANGA SHIVA KUMAR
17011070156509	K KRISHNA
17011070156510	MAVILLA BALRAJ
17011070156511	MUDAVATH SRIKANTH
17011070156512	NALLA RAJA SHEKAR
17011070156513	NENAVATH HANMANTHU

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Narayanaguda, Hyderabad. (T.S.) 500029
Final Assessment – Question Paper

Sub: PROGRAMMING LOGIC AND DESIGN

1. A VARIABLE'S _____ IN NO WAY INFLUENCES HOW THE METHOD IS CALLED FROM ANOTHER MODULE
(A) LOCAL NAME
(B) DATA TYPE
(C) SIZE
(D) DEFINITION

2. YOU CAN CIRCUMVENT POTENTIAL PROBLEM CAUSE BY A USER'S INVALID DATA ENTRIES BY _____ THE USER'S INPUT
(A) GROUPING
(B) DENYING
(C) VALIDATING
(D) MODIFYING

3. THE ORDER THAT ARRAY ELEMENTS ARE STORED IN BASE ON THEIR MEMORY ADDRESS IS THE _____ ORDER
(A) NUMERIC
(B) PHYSICAL
(C) LOGICAL
(D) INDEX

4. YOU NEED TO UNDERSTAND A SUBROUTINE'S _____ IN ORDER TO USE IT
(A) INTERNAL STRUCTURE
(B) PROGRAMMING STYLE
(C) LOCAL VARIABLES
(D) INTERFACE

5. WHEN MATCHING RECORDS BETWEEN FILES THEY MUST BE :
(A) SORTED IN THE SAME ORDER
(B) PADDED TO HAVE THE SAME LENGTH
(C) BACKED UP
(D) PRINTED

6. WHEN A MENU CONTAINS NUMEROUS OPTION, IT IS MORE ELEGANT TO USE A(N) _____ STRUCTURE THAN A SERIES OF DECISIONS.
(A) CASE
(B) LOOP
(C) IF-THEN-ELSE
(D) BATCH

7. WHAT TYPE OF VARIABLES CAN BE USED TO REDUCE UNNECESSARY COMPARISION IN A BUBBLE SORT.
(A) TEMPORARY
(B) FLAG
(C) INTEGER
(D) ARRAY

8. USUALLY A PROGRAM THAT PROCESSES AN ARRAY USES A :
(A) DECISION
(B) OUTPUT REPORT
(C) BATCH
(D) LOOP

9. WHEN MERGING FILES, A _____ IS USED TO DETERMINE WHICH FILE THE NEXT RECORD COMES FROM.
(A) COMPARISON
(B) ARRAY
(C) SEQUENCE
(4) FLAG

10. WHEN MERGING 2 FILES, WHAT CONDITION TERMINATES THE MAIN LOOP OF THE PROGRAM.
(A) WHEN THE FIRST FILE REACHS EOF
(B) WHEN THE SECOND FILES REACHES EOF
(C) WHEN BOTH FILES REACH
(D) WHEN 100 RECORDS HAVE BEEN PROCESS

11. ONE OF THE THINGS YOU NEED TO FIND OUT ABOUT A MODULE YOU WISH TO CALL IS THE:
(A) ARGUMENT LIST
(B) LOCAL VARIABLE NAMES
(C) NUMBER OF LINES CODE
(D) ORIGINAL PROGRAMMERS NAME

12. PASSING VALUES TO MODULE HELPS FACILITATE:
(A) CONFUSION
(B) ENCAPSULATION
(C) SHARING
(D) REUSE

13. WHAT IS THE LOOSEST STYLE OF COUPLING?
(A) EXTERNAL
(B) DATA-SHARED
(C) CONTROL
(D) DATA

14. WHICH OF THE FOLLOWING SORTING METHODS IS THE LEAST EFFICIENT?
(A) INSERTION
(B) SELECTION
(C) BASIC BUBBLE SORT
(D) OPTIMIZED BUBBLE SORT

15. IN A (N) _____ SORT ALL OF THE ARRAY ELEMENTS ARE MOVED TO MAKE ROOM FOR THE NEWLY SORTED ELEMENT.
(A) SWAP
(B) SELECTION
(C) BUBBLE
(D) INSERTION

16. WHAT TYPE OF VARIABLE IS USED TO HELP MOVE ARRAY ELEMENTS IN A SELECTION SORT?
(A) SWAP
(B) TEMPORARY
(C) FLAG
(D) ARRAY

17. _____ CONTAINS AN EXTRA FIELD IN EACH RECORD THAT HOLDS A POINTER TO THE NEXT LOGICAL RECORD.
(A) LINK LIST
(B) INDEX
(C) PHYSICAL MEMORY
(D) RANDOM-ACCESS MEMORY

18. A_____ APPLICATION USUALLY INVOLVES THE USER TYPING A CHOICE AT THE COMMAND PROMPT
(A) GRAPHICAL USER INTERFACE
(B) BATCH
(C) CONSOLE
(D) GUI

19. WHEN PROGRAMMER WRITES A PROGRAM ,SHE MAY LEAVE CERTAIN MODULE DEFINITIONS AS _____ WHEN WORKING ON THE MAINLINE LOGIC.
(A) SHELLS
(B) STUBS
(C) STANDARDS
(D) BATCHES

Course Completion Result

17011070111001	AMGOTHU REVANTH	Course Completed
17011070111002	A NITHIN KUMAR	Course Completed
17011070111003	ANUGULA MADHURI	Course Completed
17011070111004	ARUGONDA SAI GANESH	Course Completed
17011070111005	BANTU PRADEEP KUMAR	Course Completed
17011070111006	B GANESH	Course Completed
17011070111007	BIJIGIRI ARUN KUMAR	Course Completed
17011070111008	CHAVAN DHYANESHWAR	Course Completed
17011070111009	CHOWHAN PRAKASH	Course Completed
17011070111010	DUDDU MAHESHBABU	Course Completed
17011070129001	BANAVATH SRIRAM	Course Completed
17011070129002	BANOTH NAVEEN KUMAR	Course Completed
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17011070129004	B HARISH	Course Completed
17011070129005	BHOOTHKURI VAMSHI	Course Completed
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17011070129009	DANDUGULA NAVEEN	Course Completed
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17011070156506	DANDEM PRASHANTH	Course Completed
17011070156507	GADDA RAJU	Course Completed
17011070156508	JANGA SHIVA KUMAR	Course Completed
17011070156509	K KRISHNA	Course Completed
17011070156510	MAVILLA BALRAJ	Course Completed
17011070156511	MUDAVATH SRIKANTH	Course Completed
17011070156512	NALLA RAJA SHEKAR	Course Completed
17011070156513	NENAVATH HANMANTHU	Course Completed

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Wadi Narayanguda
Hyderabad. T.S.

Course Completion Certificate(s)

BJR GOVERNMENT DEGREE COLLEGE
Narayanguda, Hyderabad

Department of Computer Science/Applications

Certificate

This is to certify that Mr/Ms. _____ B   HARISH _____

bearing registered no. _____ 1070-12-121-003 _____ has successfully completed

the certificate course on _____ PROGRAMMING   LOGIC AND DESIGN _____

_____

held during _____ 01-08-2017 _____ to _____ 22-08-2017 _____ .

K. Sunisanga Reddy
Coordinator

Principal

Students Attendance

Students Attendance – Programming Logic and Design (2017-2018)

SNo.	Sname	01.08.17	03.08.17	03.08.17	04.08.17	07.08.17	08.08.17	09.08.17	10.08.17	11.08.17
17011070111001	AMGOTHU REVANTH									
17011070111002	A NITHIN KUMAR									
17011070111003	ANUGULA MADHURI									
17011070111004	ARUGONDA SAI GANESH									
17011070111005	BANTU PRADEEP KUMAR									
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17011070156512	NALLA RAJA SHEKAR									
17011070156513	NENAVATH HANMANTHU									

SNo.	Sname	16.08.17	17.08.17	18.08.17	19.08.17	21.08.17	22.08.17
17011070111001	AMGOTHU REVANTH						
17011070111002	A NITHIN KUMAR						
17011070111003	ANUGULA MADHURI						
17011070111004	ARUGONDA SAI GANESH						
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